Correction to "Minimum Propagation Delays in VLSI"

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In the above paper,[1] we wrote that because of

$$N_i < N_0 \beta^{-2i},$$

in which N_i is the number of wires at level i, the number of wires must decrease exponentially with their length. As was pointed out by Eric Mjolsness, this is an incorrect statement: the formula just states that the number of wires decreases exponentially with the level number. Since the total area is the same at each level and since the area of a wire increases as the square of its length, the number of wires must decrease as the inverse square of the wire length.

We apologize for any misconceptions our original statement may have caused.

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[1] C. A. Mead and M. Rem, *IEEE J. Solid-State Circuits*, vol. SC-17, pp. 773–775, Aug. 1982